DOCKET NO.: STMI07-02357

Customer No.: 23990



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

JOAQUIN TORRES ET AL.

U.S. Serial No.

10/781,565

Filed

February 18, 2004

For

PROCESS FABRICATING AN **INTEGRATED** FOR

ELECTRONIC CIRCUIT THAT INCORPORATES AIR GAPS

Group No.

2811

Examiner

Not Yet Assigned

MAIL STOP AMENDMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

The undersigned hereby certifies that the following documents:

- Postcard receipt: 1.
- Information Disclosure Statement; 2.
- Forms PTO/SB/08A and PTO/SB/08B; 3.
- Copy of French Search Report; and 4.
- Eleven (11) references. 5.

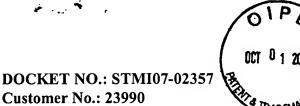
relating to the above application, were deposited as "First Class Mail" with the United States Postal Service, addressed to, MAIL STOP AMENDMENT, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on _ 2004.

William A. Munck Reg. No. 39,308

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PATENT

Customer No.: 23990

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of JOAQUIN TORRES ET AL.

U.S. Serial No. 10/781,565

February 18, 2004 Filed

PROCESS FOR FABRICATING AN INTEGRATED For

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Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

INFORMATION DISCLOSURE STATEMENT

Pursuant to the duty of disclosure under 37 C.F.R. § 1.56, Applicant submits this statement. This submittal is made in accordance with 37 C.F.R. §§ 1.97 and 1.98 and § 609 of the Manual of Patent Examining Procedure. The patents, publications and other information herein are listed below and on the attached Forms PTO/SB/08A and PTO/SB/08B. These references were located during the Search Report issued in the related French Patent Application No. 03 01978, a copy of which is attached hereto. Copies of the listed references are submitted herewith.

U.S. Patent No.	<u>Inventor</u>	<u>Date</u>
6,245,658	Buynoski	June 12, 2001
6,306,754	Agarwal	October 23, 2001
5,461,003	Havemann et al.	October 24, 1995
6,265,321	Chooi et al.	July 24, 2001

DOCKET NO.: STM107-02357 U.S. SERIAL NO.: 10/781,565

PATENT

U.S. Publication No.	Inventor	<u>Date</u>
2002/0028575	Besling et al.	March 7, 2002
2002/0089060	Morisaki et al.	July 11, 2002
2002/0090794	Chang et al.	July 11, 2002
Foreign Patent No.	Country	Date
EP 0 872 887 A2	Europe	October 21, 1998
2 784 230 (Abstract)	France	April 7, 2000

Publications

Kang-Yoon Lee et al., "Monte Carlo Simulation of Energy Dissipation in Electron Beam Lithography Including Secondary Electron Generation", J. Appl. Phys. 67 (12), June 15, 1990, Pp. 7560-7567.

Chung-Hui Chen et al., "A Novel Multi-Level Interconnect Scheme With Air As Low K Inter-Metal Dielectric For Ultradeep Submicron Application" Solid-States Electronics 45 (2001), Pp. 199-203.

Applicant hereby expressly reserves the right to swear behind the effective dates of any of the above Patents and to question the relevance and materiality of the Patents and Publications listed herein, in whole, in part, or in combination, subsequent to filing this Information Disclosure Statement.

Respectfully submitted,

DAVIS MUNCK, P.C.

Date: <u>Sept. 242004</u>

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PTO/SB/08A (08-03)

Approved for use through 07/31/2006. OMB 0651-0031

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Substitute for form 1449/PTO

Sheet 1

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Complete if Known			
Application Number	10/781,565		
Filing Date	02/18/2004		
First Named Inventor	Joaquin Torres		
Art Unit	2811		
Examiner Name	Not Yet Assigned		
Attomey Docket Number	STMI07-02357		

			U. S. PATEN	DOCUMENTS	
Examiner Initials*	Cite No.1	Document Number Number-Kind Code ^{2 (f known)}	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
	AA	^{US-} 6,245,658	06/12/2001	Buynoski	
	AB	^{US-} 6,306,754	10/23/2001	Agarwal ·	
	AC	^{US-} 5,461,003	10/24/1995	Havemann et al.	
	AD	^{US-} 6,265,321	07/24/2001	Chooi et al.	
	AE	^{US-} 2002/0028575	03/07/2002	Besling et al.	
	AF	^{US-} 2002/0089060	07/11/2002	Morisaki et al.	
	AG	^{US-} 2002/0090794	07/11/2002	Chang et al.	
		US-			

FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No.1	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages		
	<u> </u>	Country Code ³ "Number ⁴ "Kind Code ⁵ (if known)	MM-DD-YYYY		Or Relevant Figures Appear	T ⁶	
	AH	EP 0 872 887 A2	10/21/1998	NEC Corp.			
	Al	FR 2 784 230 (Abstract)	04/07/2000				
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Examiner	•	Date	
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Signature		Considered	
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Translation is attached.

This collection of information is required by 37 CFR 1.97 and 1.98. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND

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Substitute for form 1449/PTO		Complete if Known				
				Application Number	10/781,565	
INFO	ORMATION	DIS	CLOSURE	Filing Date	02/18/2004	
STA	TEMENT B	BY A	PPLICANT	First Named Inventor Joaquin Torres		
	(Use as many she	ets as n	necessary)	Art Unit	2811	
	(GGG GG Many GNG	010 43 7		Examiner Name	Not Yet Assigned	
Sheet	2	of	2	Attorney Docket Number	STMI07-02357	

		NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹				
	ВА	KANG-YOON LEE ET AL., "Monte Carlo Simulation of Energy Dissipation in Electron Beam Lithography Including Secondary Electron Generation", J. Appl. Phys. 67 (12), June 15, 1990, Pp. 7560-7567.			
	BB	Chung-Hui Chen et al., "A Novel Multi-Level Interconnect Scheme With Air As Low K Inter-Metal Dielectric For Ultradeep Submicron Application" Solid-States Electronics 45 (2001), Pp. 199-203.			
		·			

Examiner	Date	
Signature	Considered	

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1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.

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